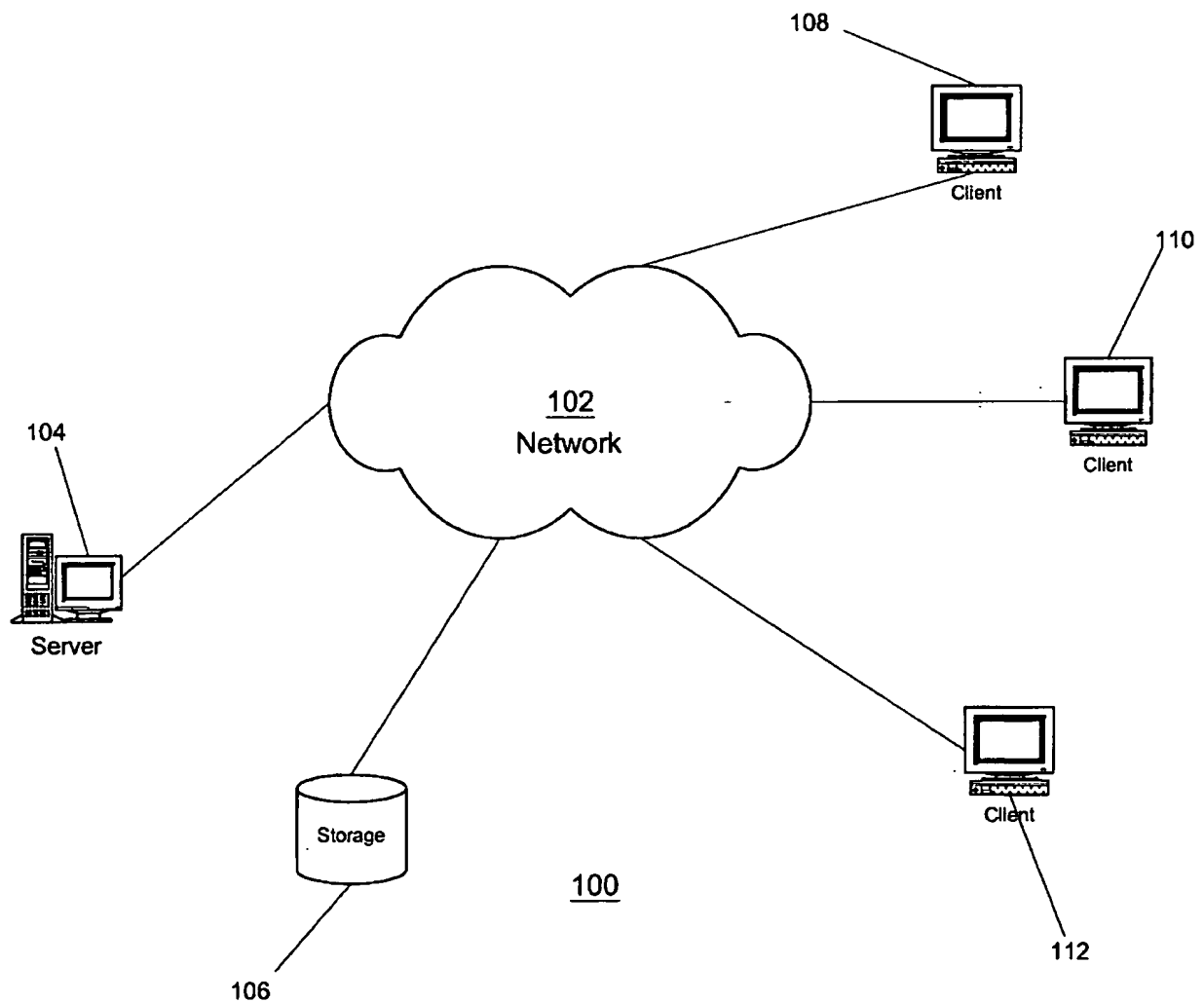


Figure 1

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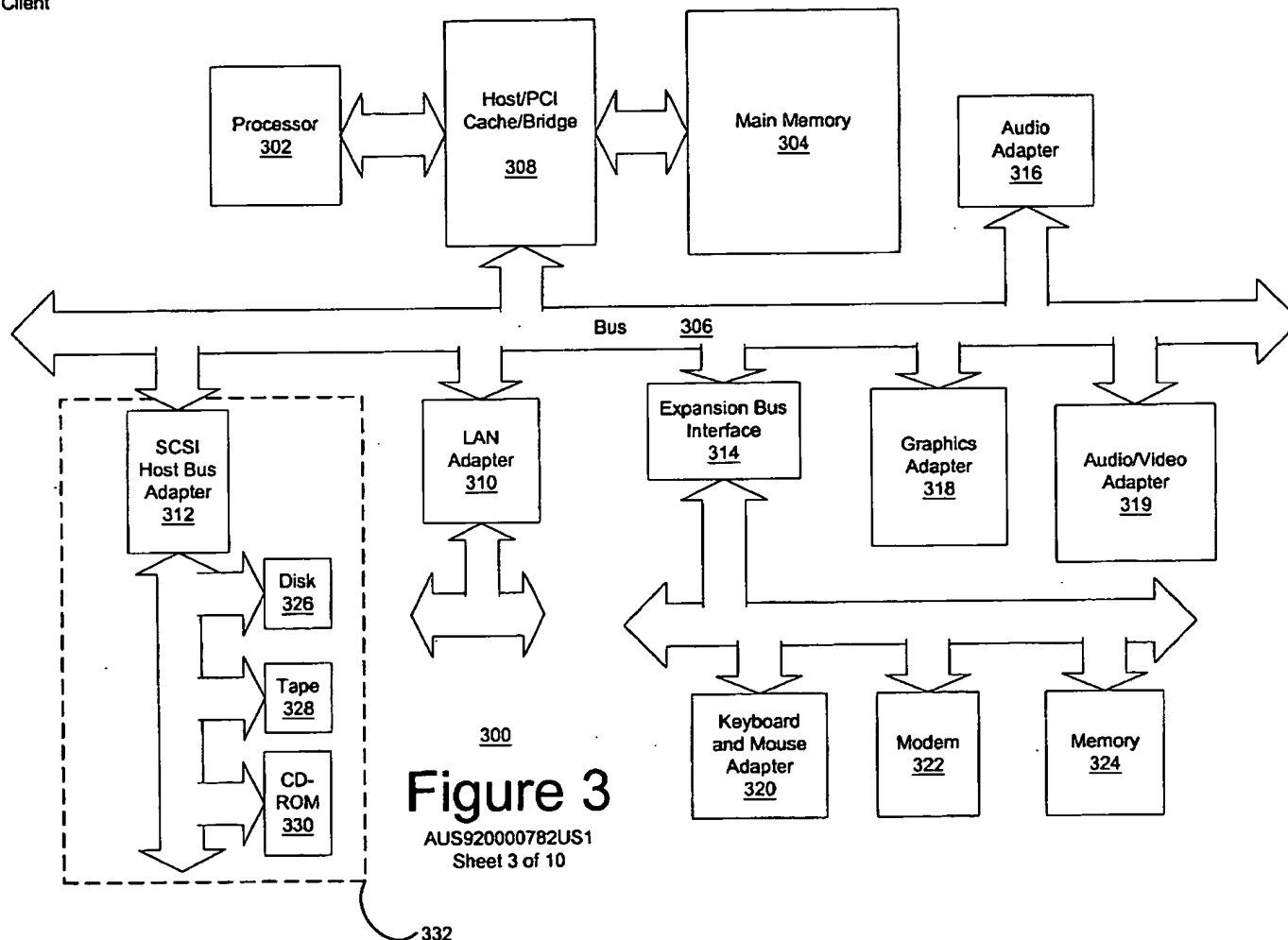


The diagram illustrates a computer system architecture. At the top, two processors, **Processor 202** and **Processor 204**, are connected to a horizontal **System Bus 206**. Below the system bus is a block containing a **Memory Controller/Cache 208** and an **I/O Bridge 210**. **Local Memory 209** is connected to the memory controller/cache. A vertical **I/O Bus 212** extends from the I/O bridge. Various components are connected to this I/O bus: **Graphics Adapter 230** and **Hard Disk 232** are connected to the left side. On the right side, three **PCI Bus Bridges** (**214**, **222**, and **224**) are connected. Each bridge is further connected to a **PCI Bus** (labeled **216**, **226**, and **228** respectively). The bridge **214** is also connected to a **Modem 218** and a **Network Adapter 220**.

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Client



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Figure 4

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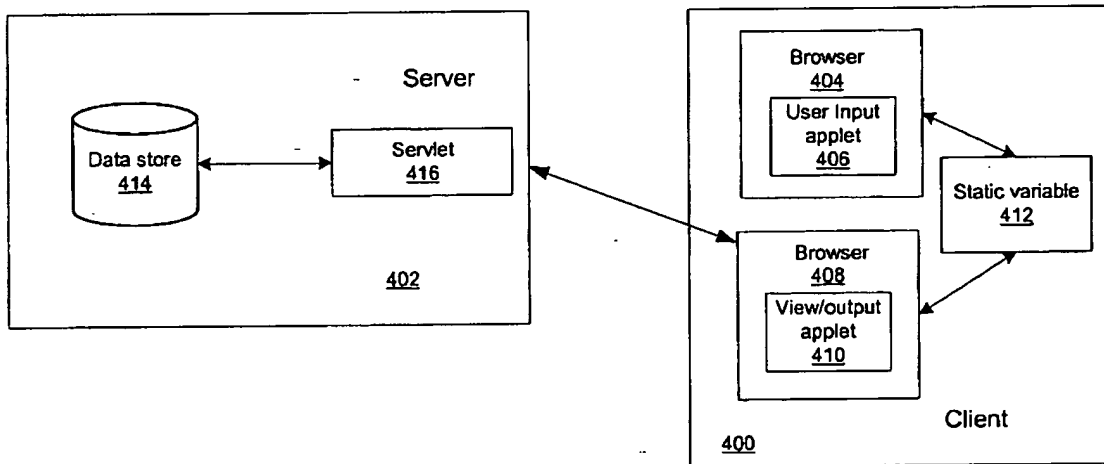


Figure 5

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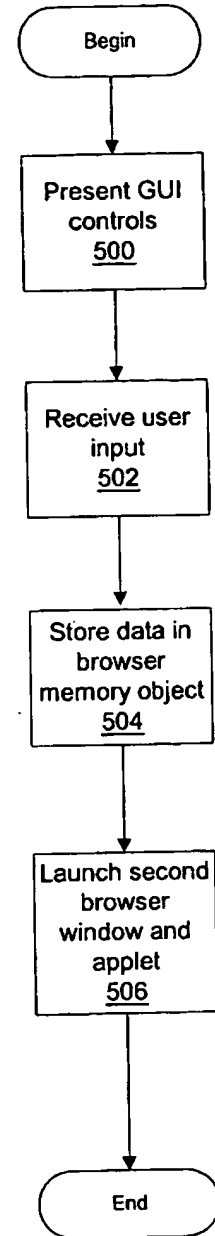
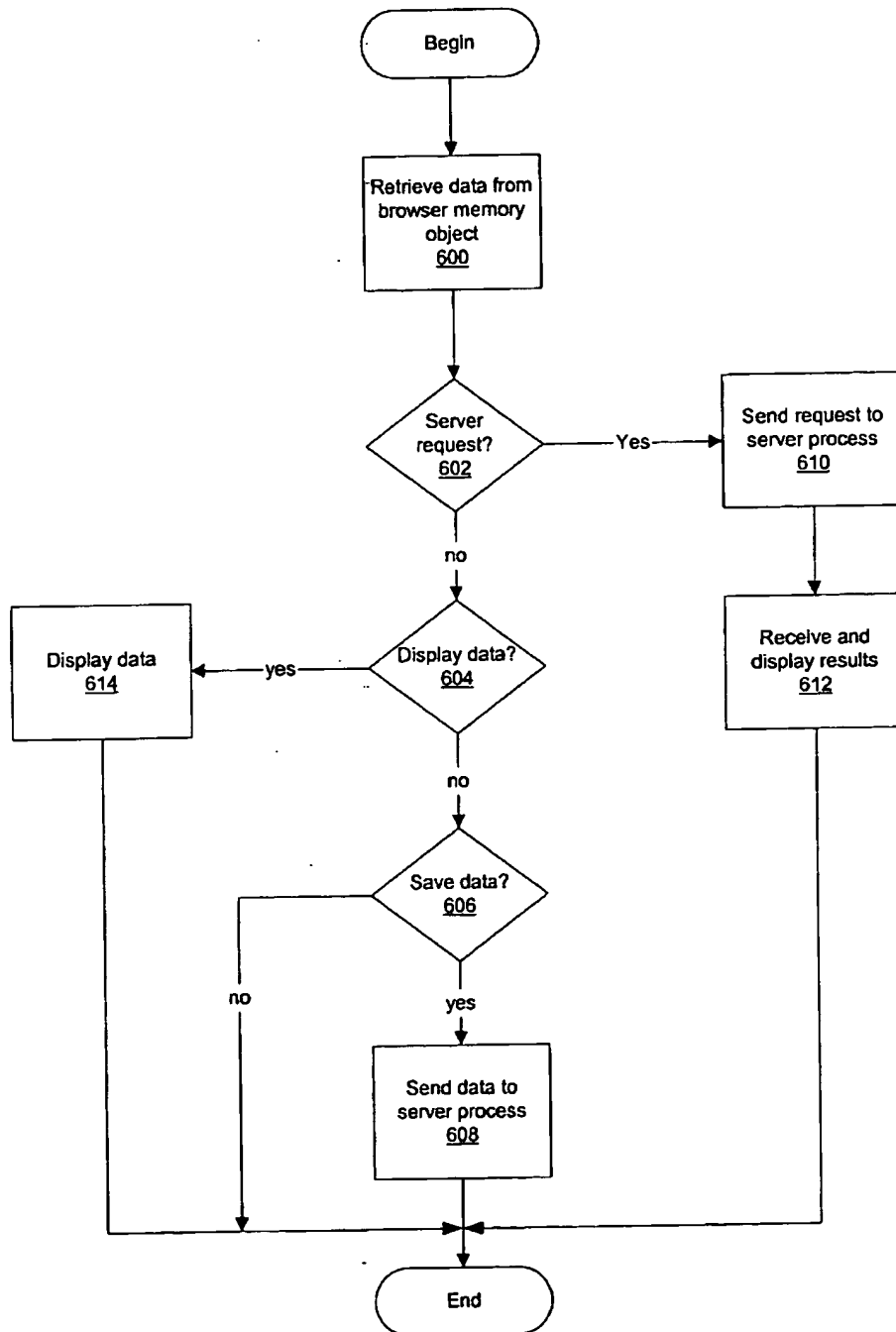


Figure 6

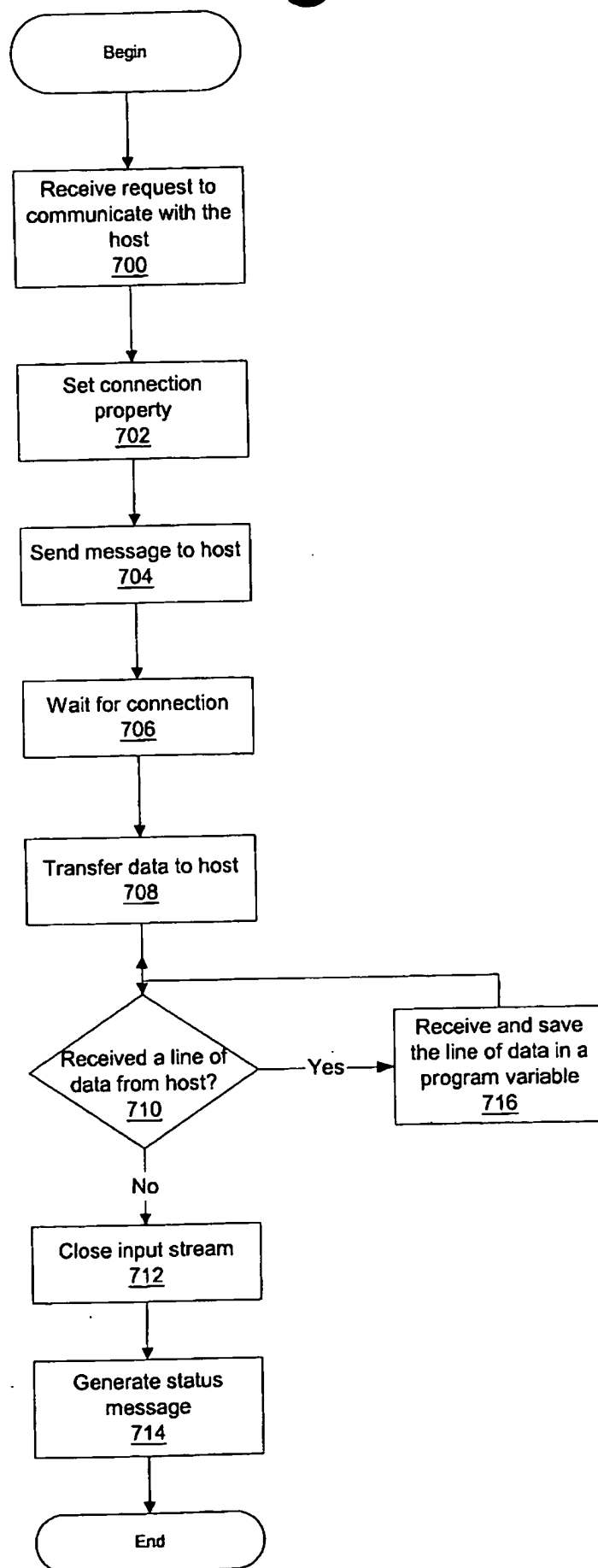
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Figure 7

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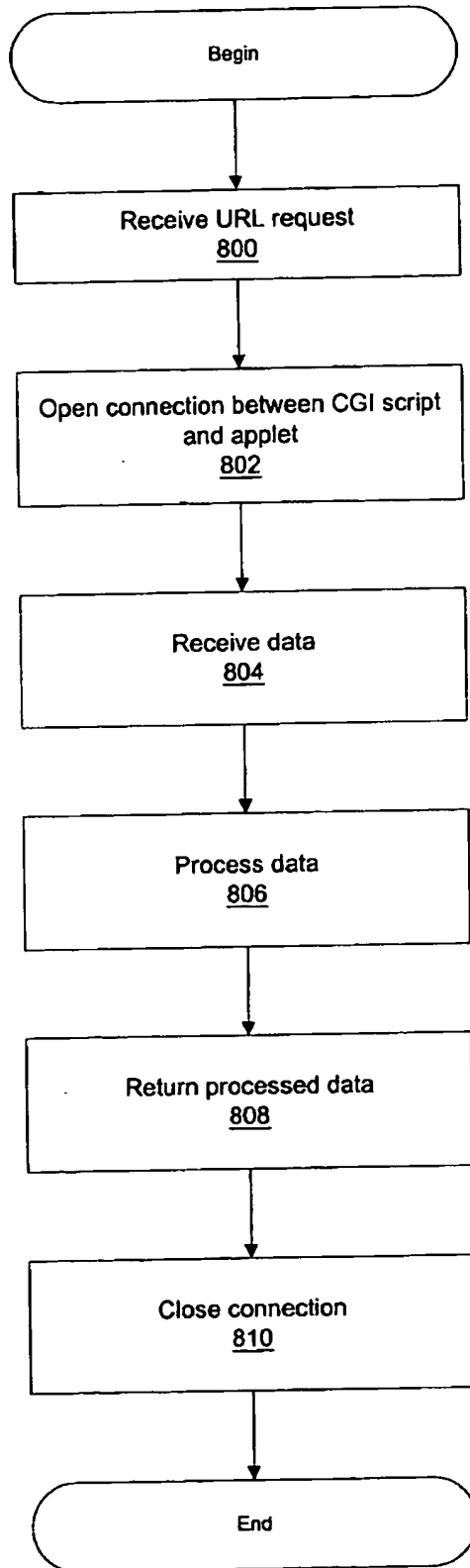
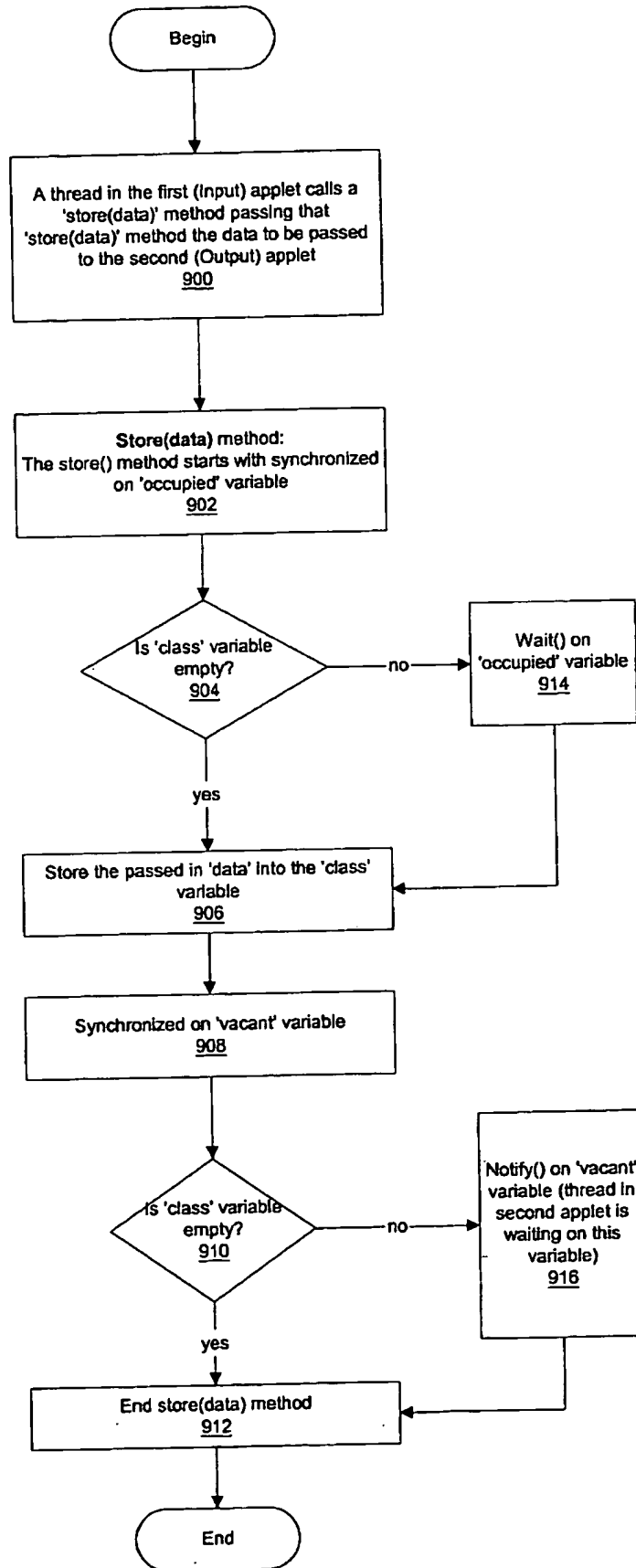


Figure 9

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store(data)

Figure 10

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